



## POS Motherboard G41-CPU-Desktop

User Manual

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With kind regards,

Your opinion:

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## POS Motherboard G41-CPU-Desktop

User Manual

Edition February 2012

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# Contents

I
2
2
3
1
5
3
)
1
۱
2
3
1
5
Ś
)
7
3
3
9
)
)
)
)
1
2
2
-
<u>-</u>
5
2
ŝ
Š
I 222717 7 1 I22 7 I 377 7 BMMMCCC112222200000

Abbreviations	41
Troubleshooting POST BIOS Beep Codes	40
BOOT BIOCK Beep Codes	
POST Code Checkpoints	
BOOTDIOCK Initialization Code Checkpoints	
Lest Points Codes.	
Load Defaults	
Discard Changes	
Discard Changes and Exit	
Save Changes and Exit	35
Exit Menu	35
Intrusion Detection [Disabled]	35
Change User Password	34
Change Supervisor Password	
Security Menu	
PS/2 Mouse Support [Auto]	
Bootup Num-Lock [On]	34
Boot Settings Configuration	
Boot Menu	
North Bridge Configuration	
Hardware Control	29
USB Configuration	28
IDF Configuration	25
CPU Configuration	24
SuperIO Information	24

## Introduction

The Motherboard G41 is the next step in the class of BEETLE-Desktop Systems. The benefit is the use of the new Express Chipset G41 designed for new generation of Intel Microprocessor family called Core 2 Duo ("Conroe" with 65nm-technology" ("Wolfdale" with 45nm ").

The two desktop microprocessor cores share the 2MB (or 6MB) L2 cache and communicate over a fast Frontside Bus (800MHz, 1066MHz and 1333MHz) with the chipset.

The G41 Motherboard is equipped with a DDR 3 SDRAM thus providing higher performance.

**This manual** provides you with all the important technical features of the G41 Motherboard and guides you through the menus of the BIOS Setup.

## **Basic Features of G41 Motherboard**

### **Processors**

The following leading edge technologies are supported:

- INTEL CORE 2 DUO E7400 (65W)
- INTEL PENTIUM E5300 (65W)
- INTEL PENTIUM E2160 (65W)
- INTEL CELERON E1500 (65W)
- INTEL CELERON 440 (35W)

## Chipset

- Platform with chipset G41 Express
- Chipset with GMCH (25W) and ICH7 (3W)

The chipset G41 has the following new features:

- Enhanced internal graphic with new graphic engine INTEL GMA X4500HD
- Use of DDR3 RAM 800 MHz and 1066 MHz up to 1GB
- Improvement of system performance by internal graphics
- Front Side Bus (FSB) 800, 1066, 1333 MHz (processor dependent)

## **More Features**

- Max. 8 USB ports (USB 2.0)
- Power-USB onboard (1x12V/3A, 1x12V/1.5A, 1x24V/3A)
- PCI Express x1 Interface
- Gigabit LAN onboard (Intel 82583V)
- Min. Main Memory 512MB; Max Main Memory 4GB
- Jumperless configuration
- Support of SATA DVD (SATA CD-ROM)
- New DVI- and PLink-Bridges (optional)
- Risercard with 1 PCI Standard connector and 1 PCI Express x1 connector
- Risercard with 2 PCI Standard connectors
- DVI-I interface onboard
- Dual independend display support
- BEETLE RMT support
- Intrusion sensor support
- Use of existing Standard-PCI Slot cards
  - RAID controller TX2650
  - PCI LPT controller
  - PCI VGA/4 controller
  - PCI COM controller

#### Attention:



Only TFT- displays with DDC (e.g. BA7xA-2 and BA8x) will be supported.

## Blockdiagram of G41 Motherboard



#### **Blockdiagram incl. Connectors**



# **Technical Data**

Supported Systems	BEETLE /MII plus, BEETLE /SII plus
Architecture:	PC- compatible with POS -specific functional units
Operating Modes:	Normal Mode S0, S2R S3, S2D S4, S5
Power Management	ACPI 3.0
Operating Systems:	WIN XP, WIN XP Embedded, POSReady 2009, WNLPOS, LINUX, Win7 prepared
Microprocessors	INTEL Core 2 Duo E7400 processor (775 Pin LGA775 package, 45 nm technology) 2.80 GHz INTEL Pentium E5300 processor (775 Pin LGA775 package, 45 nm technology) 2.60 GHz INTEL Pentium E2160 processor (775 Pin LGA775 package, 65 nm technology) 1.80 GHz INTEL Celeron E1500 processor (775 Pin LGA775 package, 65 nm technology) 2.20 GHz INTEL Celeron 440 processor (775 Pin LGA775 package, 65 nm technology) 2.0 GHz

Chipset:	INTEL chipset G41/ICH7
	Graphic and Memory Controller Hub (GMCH) with the following functions: Memory Cntrl. supports DDR3 SDRAM with 800/1066MHz System memory bus
	UMA (Unified Memory Architecture), Graphic supports 2D and 3D video streams, Plug and Play Display Functionality
	Video memory with INTEL Graphics Driver up to 1GB with WinXP (Dynamic Video Memory Technology)
	Max. 3D Resolution: 2048x1536 @ 75 Hz analog 2560x1600 @ 60 Hz digital
	I/O Controller Hub 7 (ICH7) with the following func- tions: SATA II – Cntrl., USB Cntrl. UHCI (4) and EHCI (1), Interrupt-Cntrl., DMA-Cntrl., LPC-Interface, RTC, SMBus-Host interface
Main Memory	512 MB up to max. 4GB, 2 DIMM – sockets (240pin), 1.5 V DDR3 SDRAM technology based on 512Mb and 1Gb technology, unbuffered non ECC, DDR3-800/1066 Standard DIMM, Height up to 35 mm
Clock Synthesizer	CK505 compliant RTM875T-531 from Realtek
Super I/O	IT8783F :
	6 Serial Ports, PS/2 KB & Mouse Interface, HW-Monitor
Sound Controller	ALC262 Audio Codec controller from Realtek with the following HDA functions:
	24 Bit Stereo DAC and 20 Bit Stereo ADC Mono Mic Input (MIC) Stereo Line-Level Output
Sound connection	Mono Microphone Input, Stereo Speaker Output (2 x 1,25 W@ 8 Ohm)

Riser-Card Interface	PCI-Bus (32 bit interface, 33MHz) PCI Express 1.0a, PCIe x1
BIOS	Serial Flash: 8Mbit 1024KB Flash Memory, AMI Legacy BIOS,, PCI Rev.1.0A, DMI -support
Battery	3 V Lithium for RTC and SIO Type: Sanyo/Panasonic CR2032 , 220 mAh
System Bus Frequency	max. 1333MHz
RAM Bus Frequency	max. 1066MHz
PCI Bus Frequency	PCI 2.1, 33 MHz
Wake On Feature	Wake On LAN, Wake On MODEM, Wake On Time, Wake On PS/2 & USB1,2,3,4 Wake On RMT
Keyboard Connection	PC-AT compatible
PS/2-Mouse Connection	via Y-cable together with keyboard; optional internal mouse connector for a 2 <sup>nd</sup> PS/2 connector
Serial Interfaces	COM1, COM2*, COM3(*), COM4(*), COM5(*), COM6(*)
Loudspeaker	AT-compatible, volume control defined by BIOS Setup in three steps: high- , medium- , low volume
SATA II Connection	3 ports, 3.0 Gb/s
USB Connection	General: USB 2.0 USB1, 2: Standard USB-A connectors, USB 3, 4: 2x5 pin header for 2x Front USB-A, USB 4: 1x6 pin Header (for UPS, shared with 1x front USB), USB 5: 1x6 pin header (for PowerUSB), USB 6, 7, 8: 3x Power USB (2x12V+1x24V)
Cash Drawer Connection	Up to 2 cash drawer can be connected,, connection via RJ12 connector at Power supply (only for one cash drawer)
RMT Connection	RJ10 connector

Fan Connection	2pcs 4pin connector with PWM
PCI Plug-in Card Interface	32 bit interface, 33 MHz
Status display connection	Support for LEDs: Power On and HDD activity
Current Consumption (E1500 in WinXP Idle with 2 GB RAM))	+ 3,3 V 1.59A + 5 V 0.14A + 12 V 1.46A
Max. current for keyboard	+ 5V 500 mA
Max. current per port (for COM2*, 3*, 4*,5*,6*)	+ 12V 600 mA
Max. current in total (for COM2*, 3*, 4*)	+ 12V 900 mA
Max. current per port (for COM2*, 3*, 4*, 5*, 6*)	+ 5V 300 mA
Max. current in total (for COM2*, 3*, 4*, 5*, 6*)	+ 5V 1000 mA
Max. current per port Standard USB	+ 5V 500 mA
Max. current in total Standard USB	+ 5V 2,0 A
Max. current for PowerUSB PUSB1 PUSB2 PUSB3	+ 12V: 3A + 12V: 1,5A + 24V: 3A
Fuses (Polyswitches) + 5V	COM2*, COM3*, COM4*, COM5*, COM6*, USB 6-8, DVI
+ 5VDUAL	USB1-4, PS/2
+ 5VSB	RMT
+ 12V	COM2*, COM3*, COM4*, COM5*, COM6* USB 6, USB 7
+ 24V	USB 8
Board Dimensions	255mm x 210mm

# **Plug In Cards / Risercards**

Plug In modules available for the G41 Motherboard:

- Risercard with Standard PCI Interface
- Optional Risercard with PCIe x 1
- Internal COM cable powered and unpowered version
- Support for Power USB Hub (4 x 12V)

# Connectors

## External

Interface	Connector-Type
COM1	9 pin D-sub male
COM2*	9 pin D-sub female
Keyboard, Mouse	6 pin Mini Din (PS/2)
USB 1, 2	USB-LAN Stack connector 2x USB-A
CRT (via adapter from DVI-I)	15 pin HDD-sub female
PLink (via Bridge)	40 pin Mini Delta Ribbon
DVI	24pin DVI female
LAN	USB-LAN Stack connector RJ45 female
Line Out	3,5 mm female
Microphone	3,5 mm female
USB 6, 7	4+ 4pin Power USB 12V connector
USB 8	4+ 4pin Power USB 24V connector
RMT	4pin RJ10 female

## Internal

Interface	Connector-Type
DDR3-DIMMs	2 x 240 pin micro edge connector
SATA II	3 x 7 pin Standard SATA header
PLINK- /DVI- Bridge	2 x 19 pin Header, 2 mm
USB 3, 4	2 x 5 pin Header, 2.54 mm
USB 4 for UPS	1 x 6 pin Header (JST)
USB 5	1 x 6 pin Header (JST)
PS/2-Mouse	4 pin Header (JST)
Risercard	164 pin connector (PCI Express type)
Speaker	4 pin Dubox Header
PSU	2 x 10 pin Header 2 x 9 pin Header 2 x 5 pin Header 2 x 2 pin Header (ATX)
Power On Button	4 pin Header (JST)
Status Display	4 pin Dubox Header
Fan 1,2	4 pin Header (MOLEX)
COM 3 <sup>(*)</sup> , 4 <sup>(*)</sup> , 5 <sup>(*)</sup> , 6 <sup>(*)</sup>	2 x 7 pin Header (JST)
Chassis Intrusion	3pin Header (JST)

## **Motherboard Layout**



# **Changing the Battery**

The BEETLE POS systems are equipped with a lithium battery on the motherboard (see page 13) to ensure data retention, the time and the setup parameters. The battery should be changed approximately every five years.



When inserting the new battery, make sure the polarity is correct. This is marked in the socket. Incorrect replacement of the battery may lead to the danger of explosion.

The battery is located in a socket on the Motherboard. To gain access to the battery, proceed as described in the according chapters of your BEETLE **User Manual.** 

See:

http://www.wincor-nixdorf.com/internet/uk/Services 26Support/ Support/TechnicalSupport/POSSystems/Manuals/index.html



The lithium battery must be replaced only by identical batteries or types recommended by Wincor Nixdorf International.

You can return the used batteries to your Wincor Nixdorf International sales outlet.

Batteries containing harmful substances are marked accordingly. The chemical denotations are as follows: CD = Cadmium; Pb = Lead, Li = Lith-ium.



This symbol on a battery tells you that batteries containing harmful substances must not be disposed of as household waste. Follow the country specific laws and regulations. Within the European Union you are legally bound to return these batteries to the service organization where you purchased the new battery.



The setup parameters must be reset each time the battery has been changed.

## **Description of LED**

## **PROCHOT#: CPU Temperature Indicator LED (yellow)**

The LED at location H1 (see next page) is connected to CPU signal PROCHOT# (processor hot).

This signal will go active when the processor temperature monitoring sensor detects that one or more cores have reached its maximum safe operating temperature. In this case the LED is on during S0. The LED is also active while the CPU core voltage is off during S0.

## Physical Arrangement on PCB layout



## **Addendum A: Sleep States**

- S0 Normal operation ("On")
- S3 Suspend to RAM / "Stand By"
- S4 Suspend to Disk / "Hibernation"
- S5 Soft Off

## **BIOS Setup**

The G41-CPU main board comes with an AMI BIOS chip that contains the ROM Setup information of your system. This chip serves as an interface between the processor and the rest of the main boards components. This section explains the information contained in the Setup program and tells you how to modify the settings according to your system configuration.

Even if you are not prompted to use the Setup program, you might want to change the configuration of your system in the future. For example, you may want to enable the Security Password Feature or make changes to the power management settings. It will then be necessary to reconfigure your system using the BIOS Setup program so that the system can recognize these changes and record them in the CMOS RAM or the FLASH ROM. All setup data is stored in a non volatile memory (CMOS RAM). When you remove the CMOS battery, all parameters will be lost.

## **Standard BIOS Version**

The BIOS ROM of the system holds the Setup utility. When you turn on the system, it will provide you with the opportunity to run this program. This appears during the Power-On Self Test (POST). Press <F2> to call the Setup utility. If you are a little bit late pressing the mentioned key, POST will continue with its test routines, thus preventing you from calling Setup. If you still need to call Setup, reset the system by pressing <Ctrl> + <Alt> + <Del>. You can also restart by turning the system off and then on again. But do so only if the first method fails.

If you like to change the boot order only once, you can press the <F11> key during the POST is running. At the end you will see a Pop-Up window with all the devices the system is finding. With the keys <UP> and <DOWN> you select the boot device.

The Setup program has been designed to make it as easy as possible. It is a menu-driven program, which means you can scroll through the various sub-menus and make your selections among the predetermined choices. When you invoke Setup, the main program screen will appear. Read more about the Setup entries on the following pages.

Because the BIOS software is constantly being updated, the following BIOS screens and descriptions are for reference purposes only and may not reflect your BIOS screens exactly.

#### **BIOS Menu Bar**

The top of the screen has a menu bar with the following sections:

INFO	Use this menu for information only
MAIN	Use this menu to make changes to the basic system configuration.
ADVANCED	Use this menu to enable and make changes to the advanced features.
BOOT	Use this menu to configure the default system device used to locate and load the Operating System.
SECURITY	Use this menu to enable a supervisor or user password and Intrusion Detection.
EXIT	Use this menu to exit the current menu or specify how to exit the Setup program.



To access the menu bar items, press the right or left arrow key on the keyboard until the desired item is highlighted.

#### Legend Screen

The right frame displays the key legend. The keys in the legend frame allow you to navigate through the various setup menus. The following table lists the keys found in the legend with their corresponding alternates and functions.

Navigation Key(s)	Description of Functions
$\leftarrow$ or $\rightarrow$ (keypad arrows)	Select the menu item to the left or right.
↑ or $\downarrow$ (keypad arrows)	Moves the highlight up or down between fields.
+ (plus key) - (minus key)	Change field contents.
<tab></tab>	Jumps from one field to the next.
<f1></f1>	Opens a general Help Screen with extended informations.
<f10></f10>	Saves changes and exits Setup.
<esc></esc>	Opens a windows to select between exit and return to setup

#### **General Help**

In addition to the Item Specific Help window, the BIOS setup program also provides a General Help screen. This screen can be called from any menu by simply pressing <F1>. The General Help screen lists the legend keys with their corresponding alternates and functions.

#### Scroll Bar

When a scroll bar appears to the right of a help window, it indicates that there is more information to be displayed that will not fit in the window. Use <PgUp> and <PgDn> or the up and down keys to scroll through the entire help document.

Press <Home> to display the first page, press <End> to reach the last page. To exit the help window, press the <Enter> or <Esc> key.

#### Sub-Menu



Note that a right pointer symbol appears left of certain fields. This pointer indicates that a sub-menu can be launched from this field. A sub-menu contains additional options for a field parameter. To call a sub-menu, simply move the highlight to the field and press <Enter>. The sub-menu then will appear immediately. Use the legend keys to enter values and move from field to field within a sub-menu just as you would do within a menu. Use the <Esc> key to return to the main menu.

Take some time to familiarize yourself with each of the legend keys and their corresponding functions. Practice navigating through the various menus and sub-menus. If you accidentally make unwanted changes to any of the fields, use the set default hot key <F9>. While moving around through the Setup program, note that explanations appear in the Item Specific Help window located to the right side of each menu. This window displays the help text for the currently highlighted field.

## Info Screen

When the Setup program is accessed, the following info screen appears:

Product Name: BIOS Version:	G41-CPU-STANDARD WN STD xx/yy mm/dd/yyyy
MAC-Address:	00-21-05-16-EE-B2
UUID Info:	FF-FF-FF-FF-FF-FF-FF FF-FF-FF-FF-FF-FF-F
System:	####
Mainboard:	
PwrSupply:	

This screen is for information only. There is nothing that could be changed within Setup.

All information is intended to facilitate the support of your system.

#### **Product Name:**

This text is fixed for your mainboard with standard BIOS. This board is also called "G41-CPU-STANDARD".

#### **BIOS Version:**

The BIOS version is displayed in the release format xx/yy, followed by date of release in international format.

#### MAC Address:

The Ethernet MAC-Address of the Onboard LAN Controller is displayed at this line if [Enabled].

#### UUID Info:

A UUID is an identifier standard used in software construction, standardized by the Open Software Foundation. The intent of UUIDs is to enable distributed systems to uniquely identify information without significant central co-ordination.

#### System, Main board, Power Supply:

The default placeholders may be replaced by specific data from factory, describing configuration, serial number etc. for each device.

### Main Menu

System Overview	
Processor: Intel ® Core™2 Duo CPU 4300 Speed : 1800MHz Count : 1	@ 1.80GHz
System Memory: Size :478MB	
System Time: System Date:	[08:14:46] [01/31/2011]

#### System Time [XX: XX: XX]

Sets your system to the time that you specify (usually the current time). The format is hour, minute, second. Valid values for hour, minute, and second are: Hour: (00 to 23), Minute: (00 to 59), Second: (00 to 59). Press <Enter> to terminate every entry value and reach the next position. On the upper right frame find the keys listed to modify the values.

#### System Date [XX/XX/XXXX]

Sets your system to the date that you specify (usually the current date). The format is month, day, year. Valid values for month, day and year are: Month: (1 to 12), Day (1 to 31), Year: (up to 2079).

### **Advanced Menu**

Advanced Settings

Hardware Monitoring SuperIO Information

WARNING: Setting wrong values in the sections below may cause system to malfunction,

CPU Configuration IDE Configuration USB Configuration Hardware Control North Bridge Configuration

#### **Hardware Monitoring**

Hardware Monitoring	
CPU Temperature Sensor	55°C/131°F
Board Temperature Sensor	39°C/102°F
Fan1 Speed	1439 RPM
Fan2 Speed	N/A
PSU Speed	2636 RPM
CPU Core	1.280 V
+1.50V	1.504 V
+3.30V	3.376 V
+12.0V	12.480 V
-12.0V	-12.049 V
+1.10V	1.104 V
+1.10V	1.104 V
VBAT	3.072 V

CPU/Board Temperature Sensors [xx °C/xxx °F]

The onboard hardware monitor is able to detect the motherboard and CPU temperatures (for supported processors only).

Fan\_x Speed [xxxx rpm]

The onboard hardware monitor is able to detect 2 fan speeds and the power supply fan speed in rotations per minute (rpm).

Several Voltages [xx.x V]

The onboard hardware monitor is able to detect the voltage output by the onboard voltage regulators.

#### **SuperIO Information**

Display Ite8783 Super IO Configuration	
Serial Port1 Address	[3F8]
Serial Port1 IRQ	[4]
Serial Port2 Address	[2F8]
Serial Port2 IRQ	[3]
Serial Port3 Address	[3E8]
Serial Port3 IRQ	[10]
Serial Port4 Address	[2E8]
Serial Port4 IRQ	[11]
Serial Port5 Address	[2E0]
Serial Port5 IRQ	[6]
Serial Port6 Address	[2F0]
Serial Port6 IRQ	[5]

This setup screen shows the programmed values of the 6 onboard legacy serial ports. This is an on-screen information only (no options).

### **CPU Configuration**

Configure a	dvanced CPU settings	
Manufacture Intel ® Core Frequency FSB Speed Cache L1 Cache L2	er: Intel ™2 Duo CPU 4300 :1.80GHz :800MHz :64 KB :2048 KB	@ 1.80GHz

Ratio Actual Value :9	
Max CPUID Value Limit Execute-Disable Bit Capability	[Disabled] [Enabled]
Core Multi-Processing	[Enabled]

The first lines lists infos about the installed CPU. You can use this screen to select options for the CPU Configuration. The content of the CPU configuration setup screen varies depending on the installed processor. On the upper right frame you can see a short description of each changeable setup point.

#### Max CPUID Value Limit

Allows you to determine whether to limit CPUID maximum value. Set this item to [Disable] for Windows XP operating system, set this item to [Enabled] for legacy operating system. Configuration options: [Disabled] [Enhanced]

#### Execute-Disable Bit Capability

Enable or disables Intel Execute Disable Bit function. This function may enhance protection for the computer, reducing exposure to viruses and malicious buffer overflow attacks when working with supporting software. Configuration options: [Disabled] [Enhanced]

#### Core Multi-Processing

Allows you to determine whether to enable all CPU cores and multithreading function when using an Intel CPU that supports multi-core technology. This feature only works for operating systems that support multiprocessor mode. Configuration options: [Disabled] [Enhanced]

#### **IDE Configuration**

	IDE Configuration	
	ATA/IDE Configuration Port3 SATA AHCI Speed: GEN 1	[Enabled] (1.5 Gb/sec)
AAA	Primary IDE Master Primary IDE Slave Secondary IDE Master	[Not Detected] [Hard Disk] [Not Detected]
	IDE Detect Time Out (Sec)	[35]

The onboard printed SATA port numbers have the following references to the setup entries:

- Port 1 Primary IDE Master
- Port 2 Secondary IDE Master
- Port 3 Primary IDE Slave.

#### ATA/IDE Configuration

With this setup point the user has the choice between disabling all IDE ports and using them. Configuration options: [Disabled] [Enabled]

Primary IDE Master/Slave & Secondary IDE Master

Primary IDE	- Slave		_
Device Vendor Size LBA Mode Block Mode PIO Mode Async DMA Ultra DMA S.M.A.R.T.	:Hard Disk :TOSHIBA MK1637GSX :160.0GB :Supported :16Sectors :4 :MultiWord DMA-2 :Ultra DMA-5 :Supported		
Type: LBA/Large I Block (Multi PIO Mode DMA Mode S.M.A.R.T. 32Bit Data	Mode -Sector Transfer) Transfer	[Auto] [Auto] [Auto] [Auto] [Auto] [Auto] [Enabled]	

The first lines are info lines about the attached S-ATA hard disks, while the next two are used for the P-ATA disks.



**Note**: Before attempting to configure a hard disk drive, make sure you have the configuration information supplied by the manufacturer of the drive. Incorrect settings my cause your system not to recognize the installed hard disk. To allow the BIOS to detect the drive type automatically, select [Auto].

#### Type [Auto]

Select [Auto] to automatically detect an attached SATA drive. If automatic detection is successful, the correct values will be filled in for the remaining

fields on this sub-menu. If automatic detection fails, your hard disk drive may be too old or too new. You can try to update your BIOS or to enter the SATA drive parameters manually.

For special cases you can select a drive type here. The options are [Not Installed] [Auto] [CD/DVD] [ARMD]

#### LBA/Large Mode [Auto]

LBA (Logical Block Addressing) is a method of addressing data on a disk drive. The options are [Auto] [Disabled]

#### Block (Multi-Sector Transfer) [Auto]

This option sets the block multi sector option. If [Disabled] the data transfer from and to the device occurs one sector at a time. If [Auto] is selected all data transfer will be done with multiple sectors at a time. The options are [Auto] [Disabled]

#### PIO Mode [Auto]

This option lets you set a PIO (Programmed Input/Output) mode for the IDE device.

Modes 0 trough 4 provides successively increased performance. Configuration options: [Auto] [0] - [4]

#### DMA Mode [Auto]

This field can increase the performance like the PIO Mode entry, two Configuration options: [Auto] [SWDMAx] [MWDMAx] [UDMAy] (x=0-2, y=0-5)

#### S.M.A.R.T. [Auto]

Self-Monitoring Analysis and Reporting Technology (S.M.A.R.T.) feature can help to predict impending drive failure. This feature allows your system to report read/write errors of the hard drive and to issue warnings when a third party hardware monitor utility is installed. Configuration options: [Auto] [Enabled] [Disabled]

#### 32Bit Data Transfer [Enabled]

This field sets the 32-bit data transfers option. Configuration options: [Disabled] [Enabled]. IDE Detect Time Out (Sec)

This field selects the time out value for detecting ATA/ATAPI devices.

#### **USB** Configuration

USB Configuration USB Devices Enabled : 1 Mouse, 1 Drive Legacy USB Support [Enabled] USB 2.0 Controller Mode [HiSpeed] BIOS EHCI Hand-Off [Enabled] USB 2.0 Controller [Enabled] USB Mass Storage Device Configuration

Legacy USB Support [Enabled]

This motherboard supports Universal Serial Bus (USB) devices. Normally if this option is not enabled, any attached USB mouse or USB keyboard will not become available until a USB compatible operating system is fully booted with all USB drivers loaded. When this option is enabled, any attached USB mouse or USB keyboard can control the system even when there is no USB drivers loaded on the system. If you like to use a USB-Floppy disk or a USB CD-ROM device for booting, you have to enable this setup point and after detecting of this USB device from the BIOS, you have to switch the boot order to the appropriate device. In the AUTO mode is the USB support switched off, when no Legacy USB device was found. Configuration Options: [Disabled] [Enabled] [Auto]

#### USB 2.0 Controller Mode [HiSpeed]

This option can reduce the transfer rate of a USB Hispeed device to Fullspeed. Configuration Options: [HiSpeed] [FullSpeed]

#### BIOS EHCI Hand-Off [Enabled]

This entry is useful for the EHCI handling in some Windows operating systems like Windows XP. Mostly this option must be enabled. Configuration Options: [Enabled] [Disabled]

#### USB 2.0 Controller [Enabled]

This option switches physical ON or OFF the EHCI controller inside of the chipset. When [Disabled] the booting operating system cannot see an EHCI controller. Configuration Options: [Enabled] [Disabled]

#### USB Mass Storage Device Configuration

This entry will only be displayed, if a USB Mass Storage device was detected. Is your device not correctly detected, you can select a longer 'Reset Delay' time and another 'Emulation Type' of the device.

#### **Hardware Control**

Hardware Control	
Reset Config Data	[No]
Audio Controller	[Enabled]
LAN Controller	[Enabled]
Volume Control	[High]
COM Routing	[DSUB]
Primary IGD Display is on	[DVI-I]
Display Output to COM3	[No]
Restore on AC Power Loss	[Last State]
USB+PS/2 Wakeup from S3/S4	[Enabled]
Wake On Ring	[Disabled]

#### Reset Config Data [No]

[Yes] erases all configuration data in NVRAM (Non Volatile RAM) area and create a new table of hardware resources. Configuration Options: [No] [Yes]

If you are facing problems after adding or removing any hardware components to the system it might be wise to select the [Yes] option once. This allows the BIOS to reconfigure available hardware resources.

#### Audio Controller [Enabled]

Setting item to [Enable] will allow the onboard audio to operate properly. Setting item to [Disabled] will remove the onboard audio controller from PCI config space. Configuration Options: [Enabled] [Disabled]

#### LAN Controller [Enabled]

This point switches physical ON or OFF the Onboard LAN Controller. The PXE Boot ROM will be loaded, when the LAN Controller is enabled. Configuration Options: [Disabled] [Enabled]

#### Volume Control [High]

This field is for the volume control of the installed speaker. Configuration options [High] [Mid] [Low].

#### COM Routing [DSUB]

Using a Touch Screen you may select routing it over COM Port 4 serial interface. Configuration Options: [DSUB] [PLINK]

#### Primary IGD Display is on [DVI-I]

If two displays are connected to the Internal Graphics Device (IGD), you have to define the one that is used for POST messages. The running operating system thereafter can initialize and use both displays. Configuration Options: [DVI-I] [PLINK/DVI]

#### Display Output to COM3 [No]

Some systems may be configured without a full screen display, just using a small display connected to the COM3 serial port. [Enabled] will redirect diagnostic information during PowerOnSelfTest to this serial port, giving control about the system to smaller displays as well. Configuration Options: [No] [Yes]

#### Restore on AC Power Loss [Last State]

Select whether you want your system to be rebooted after power has been interrupted. [Power off] leaves your system off and [Last State] reboots your system if it was active before power loss. Is the key [Power On] selected, the system will startup anytime power is available.

Configuration options: [Power off] [Last State] [Power On].

In mode [Power On] the front button is disabled. This means that there is no way to force down the system pressing the front button for more than 4 seconds, avoiding accidental shutdown.

#### USB+PS/2Wakeup from S3/S4 [Enabled]

You may select to power some devices during Standby- or Hibernate-Mode, to enable them to wake up the system. If enabled, the PS/2 connector and the stacked onboard rear USB connector are connected to 5V-Standby power. Configuration Options: [Enabled] [Disabled]

#### Wake-on Modes

Please note that you have to shut down the system in power saving modes by OS before you can use Wake-on modes. Switching off the system by mainpower switch or frontbutton-override will not initialize system wakeup functions. See following table, which wakeup events are available from different power states:

	Standby (S3)	Hibernate (S4)	Soft off (S5)
Front Button	Yes	Yes	Yes
LAN	Yes	Yes	Yes
Modem (Note1)	Yes	Yes	Yes
Time	Yes	Yes	Yes
PS/2 (Note2)	Yes	Yes	No
USB (Note2)	Yes	Yes	No



Note 1: "Yes" is valid only, if the option is [Enabled].

Note 2: "Yes" is valid only, if Standby Power is [Enabled].

#### Wake On Ring [Disabled]

This allows enabling or disabling power up the BEETLE when the modem receives a call while the BEETLE is in Soft-Off or Hibernate mode.



NOTE: The BEETLE cannot receive or transmit data until the system and applications are fully running, thus connection cannot be made on the first try. Turning an external modem off and then back on while the BEETLE is off causes an initialization string that will cause the system to power on. Configuration options: [Disabled] [Enabled].

#### North Bridge Configuration

North Bridge Chipset Configuration	
Memory Remap Feature	[Disabled]
Initiate Graphic Adapter IGD Graphics Mode Select	[PCI/IGD] [Enabled, 32MB]

Memory Remap Feature [Disabled]

This function is useful when using a 64bit OS and the board has 4GB of memory installed. Is the function [Enabled] the 4<sup>th</sup> GB of RAM will be swapped into the 4<sup>th</sup> to 5<sup>th</sup> GB area. Is the Remap function [Disabled] the OS has aprox. 3.2 GB of RAM available and the other memory is not useable. Configuration Options: [Enabled] [Disabled]

Initiate Graphic Adapter [PCI/IGD]

This function configures the primary screen, if an additional PCI Graphics card is inserted in the system. Configuration Options: [PCI/IGD] [IGD]

IGD Graphics Mode Select [Enabled, 32MB]

This option selects the amount of memory used by the Internal Graphics Device. Configuration Options: [Disabled] [Enabled, 32MB] [Enabled, 64MB] [Enabled, 128MB]

### **Boot Menu**

The Boot Menu enables you to set the order of bootable devices to a regular base. Pressing the function key <F11> while POST is running will change the boot order only once. You will see a Pop-Up window listing all devices the system is able to boot from. Select the boot device with keys <Up> and <Down>. Press <Enter> key to start the selected device booting.

Please select Boot Device	
USB	Scan Disk U3 Cruzer Micro 8.0
Network	IBA GE SLOT 0200 v 1353

Boot Settings	
Boot Settings Configuration	
1 <sup>st</sup> Boot Device 2 <sup>nd</sup> Boot Device 3 <sup>rd</sup> Boot Device 4 <sup>th</sup> Boot Device Hard Disk Drives <i>Hard Disk Drives</i>	[Not Installed] [Not Installed] [SATA: TOSHIBA MK163] [Network:IBA GE Slo]
1 <sup>st</sup> Drive 2 <sup>nd</sup> Drive	[SATA:TOSHIBA MK163] [USB:B4F SLIM]

#### 'N' Boot Device

These menu entries are used to specify the boot sequence from the available devices. Every boot device (from 1 till 4) specifies a device group. The groups are Removable, Optical, Hard Disk, and ROM Devices from top to bottom. Is more than one identical device added, an additional menu point below the boot devices will be visible. Inside of this group you specify the boot order of the identical devices. Below you can see the contents of the Hard Disk Drives. So, the boot order will be:

1<sup>st</sup> SATA: TOSHIBA, 2<sup>nd</sup> USB:B4F SLIM 3<sup>rd</sup> Network: IBA GE

#### **Boot Settings Configuration**

Boot Settings Configuration	
Quick Boot	[Enabled]
Bootup Num-Lock	[On]
PS/2 Mouse Support	[Auto]

#### Quick Boot [Enabled]

Allows the BIOS to skip some tests during the POST is running. This will decrease the time needed to boot the system. Configuration Options: [Enabled] [Disabled]

#### Bootup Num-Lock [On]

Select the Power-On state for Numlock of an attached keyboard. Configuration Options: [On] [Off]

#### PS/2 Mouse Support [Auto]

Select the PS/2 Mouse Support of the System. Configuration Options: [Auto] [Enabled] [Disabled]

## **Security Menu**

Security Settings

Supervisor Password: Not Installed User Password :Not Installed

Change Supervisor Password Change User Password

Intrusion Detection

[Disabled]

#### **Change Supervisor Password**

This field allows you to set the password. Highlight the field and press <Enter>.

Type a password and press <Enter>, you can type up to eight alphanumeric characters. Symbols and other characters are ignored. To confirm the password, type the password again and press <Enter>. The password is now set to [Enabled]. This password allows full access to the BIOS Setup menu.

To clear the password, highlight this field and press <Enter>. The same dialog box as above will appear. Press <Enter> and the password will be set to [Disabled].

#### **Change User Password**

This field allows you to set the password. Highlight the field and press <Enter>.

Type a password and press <Enter>, you can type up to eight alphanumeric characters. Symbols and other characters are ignored. To confirm the password, type the password again and press <Enter>. The password is now set

to [Enabled]. This password allows full access to the BIOS Setup menu.

To clear the password, highlight this field and press <Enter>. The same dialog box as above will appear. Press <Enter> and the password will be set to [Disabled].

#### Intrusion Detection [Disabled]

If the system cover is removed and the Intrusion Detection is [Enabled], the system stops during the next reboot or power up process and display a warning message. After this warning the boot process stops and the user has to go into the BIOS setup which resets the open case detection automatically. Additionally is a viewing point of the case open switch below the enable/disable entry point placed. This message will signalize the actual case open status directly. Configuration Options: [Disabled] [Enabled]

## Exit Menu

Exit Options

Save Changes and Exit Discard Changes and Exit Discard Changes

Load Defaults

Once you have made all your selections from the various menus in the Setup program, you should save your changes and exit Setup. Select Exit from the menu bar to display the following menu.

#### Save Changes and Exit

Once you have finished making selections, choose this option from the Exit menu to ensure the values you selected are saved to the CMOS RAM. The CMOS RAM is sustained by an onboard backup battery and stays on even when the BEETLE is turned off. Once this option is selected, a confirmation is asked. Select [Ok] to save changes and exit.

#### **Discard Changes and Exit**

This option should only be used if you do not want to save the changes you have made to the Setup program. If you have made changes to fields other than system date, system time, and password, the system will ask for confirmation before exiting.

#### **Discard Changes**

This option doesn't save the changes at all like the entry 'Discard Changes and Exit'. After saying [Ok], the cursor is jumping up to 'Save Changes and Exit.

#### Load Defaults

This option allows you to load the default values for each of the parameters on the Setup menu. When this option is selected or if <F9> is pressed, a confirmation is requested. Select [Ok] to load default values. You can now select Exit Saving Changes or make other changes before saving the values to the non-volatile RAM.

### **Test Points Codes**

At the beginning of each POST routine, the BIOS outputs the test point error code to I/O port address 80h. Use this code during trouble shooting to establish where the system failed and what routine has been performed.

## **Bootblock Initialization Code Checkpoints**

POST Code	Description
(Hex)	
D0	Early Boot Strap Processor initialization like microcode update,
	zation is done.
D1	Early super I/O init is done including RTC and keyboard controller. NMI is disabled. Perform keyboard controller BAT test. Save power-on CPUID value in scratch CMOS. Go to flat mode with 4 GB limit and GA 20 enabled.
D2	Verify the boot block checksum. System will hang here if check- sum is bad.
D3	Disable CACHE before memory detection. Execute full memory sizing module. Start memory refresh and do additional chipset initialization. Re-enable CACHE.
D4	Test base 512 KB memory. Adjust cache first 8 MB and set stack.
D5	Bootblock code is copied from ROM to lower system and control is given to it. BIOS now executes out of RAM. Copies BIOS from ROM to RAM for faster access. Perform main BIOS checksum and updates recovery status accordingly.
D6	If BIOS recovery is necessary, control flows to the recovery mod- ule.
D7	Restore CPUID value back into register. The Bootblock-Runtime interface module is moved to system memory and control is given to it.
D8	The Runtime module is uncompressed into memory. CPUID in- formation is stored in memory.
D9	Store the Uncompressed pointer for future use in PMM. Copying Main BIOS into memory. Leaves all RAM below 1 MB Read-Write including E000 and F000 shadow areas but closing SMRAM.
DA	Restore CPUID value back into register. Give control to BIOS POST.
DC	System is waking from ACPI S3 state.

## **POST Code Checkpoints**

POST Code	Description
(Hex)	
03	Disable NMI, Parity, Video for EGA, and DMA controllers. Init
	BIOS, POST Runtime data area. Also init BIOS modules on POST
	entry and GPNV area. Init CMOS
04	Check CMOS diagnostic byte to determine if battery power is OK
	and CMOS checksum is OK. If the CMOS checksum is bad, up-
	date CMOS with power-on default values and clear passwords.
	Init data variables that are based on CMOS setup questions. Init
	both 8259 compatible PICs in the system.
05	Init the interrupt controlling hardware and interrupt vector table.
06	Do R/W test to CH-2 count reg. Init CH-0 as system timer. Enable
	IRQ-0 in PIC for system timer interrupt.
07	Fixes CPU POST interface calling pointer.
80	Initializes the CPU. The BAT test is being done on KBC. Program
	the keyboard controller command byte after auto detection of
0.0	NB/MS is done.
	Detects the presence of DS/2 meyee
	Detects the presence of Keyboard in KPC part
00	Testing and initialization of different input devices. Also, update
VE	the kernel variables. Uncomprose all available RIOS logo and
	Silent logo modules
13	Farly POST init of chinset registers
20	Relocate System Management Interrunt vector for all CPU in the
20	system.
24	Uncompress and init any platform specific BIOS modules.
2A	Init different devices through DIM
2C	Detection and init of the video adapter installed in the system that
	have optional ROMs.
2E	Initializes all the output devices.
31	Allocate memory for ADM module and uncompress it. Give control
	to ADM module for initialization. Activate ADM module.
33	Init the silent boot module. Set the window for displaying text in-
	formation.
37	Displaying sign-on message, CPU information, setup key mes-
	sage, and any OEM specific information.
38	Initializes different devices through DIM.
39	Init DMA-1 & DMA-2
3A	Init RTC date/time.
3B	Test for total memory installed in the system. Also, check for F2 or
	ESC keys to limit memory test. Display total memory in the sys-
	tem.
3C	Mid POST initialization of chipset registers

40	Detect different devices (parallel ports, serial ports, and coproces-
	sor in CPU, etc.) successfully installed to the system and update
50	Line DDA, EDDA, etc
52	Allocates memory for Extended BIOS Data Area from base mem
	Allocates memory for Extended BIOS Data Area from base mem-
	that peeds an adjustment in system RAM size if peeded
60	Initializes NI IM-I OCK status and programs the KBD typematic
	rate.
75	Initialize INT-13 and prepare for IPL detection.
78	Initialize IPL devices controlled by BIOS and option ROMs.
7C	Generate and write contents of ESCD in NVRAM.
84	Log errors encountered during POST.
85	Display errors to the user and gets the user response for error.
87	Execute BIOS setup if needed / requested. Check boot password
	if installed.
8C	Late POST initialization of chipset registers.
8D	Build ACPI tables.
8E	Program the peripheral parameters. Enable NMI.
90	Initialization of system management interrupt by invoking all han-
	diers.
A1	Clean-up work needed before booting to OS.
A2	Takes care of runtime image preparation for different BIOS mod-
	ules. Initializes the Microsoft IRQ Routing Table.
A4	Display boot option popup menu.
A7	Displays the system configuration screen if enabled. Initialize the
	CPU before boot, which includes the programming of the MTRRS.
A9	Wait for user input at config display if needed.
	Uninstall POST INTICh vector and INTU9h vector.
AB	Prepare BBS for INT 190 boot. Init MP tables.
AC	ADM module
D1	ADM module.
ы	final MTRR values
<u> </u>	Farly CPU init start disable Cache, init Local APIC
C1	Set up boot strap processor for Information
C2	Set up boot strap processor for POST
C5	Enumerate and set up application processors
C6	Re-enable cache for boot strap processor.
C7	Farly CPU init exit.
00	Passes control to OS Loader (typical INT19h).

## **Boot Block Beep Codes**

Number of	Description
Beeps	
1	No Media present.
2	'BIOS.ROM' file not found in root directory.
3	Insert next device if multiple devices are used for recovery.
4	Flash Programming successful.
5	File read error.
7	No Flash EPROM detected.
10	Flash Erase error.
11	Flash Program error.
12	'BIOS.ROM' file size error.
13	BIOS.ROM image mismatch (file layout does not match image
	present in hash device).

## **POST BIOS Beep Codes**

Number of	Description
Beeps	
1	Memory refresh timer error.
3	Base memory read/write test error
6	Keyboard controller BAT command failed
7	General exception error (processor exception interrupt error)
8	Display memory error (system video adapter)

## **Troubleshooting POST BIOS Beep Codes**

Number of	Description
Beeps	
1, 2	Intrusion sensor is activated
Constant	No memory module included.

# **Abbreviations**

ADM	AMI Display Manager
ACPI	Advanced Configuration and Power Interface
AGTL+	Assisted Gunning Transceiver Logic
APC	Advanced Power Control
APIC	Advanced Peripheral Interrupt Controller
APM	Advanced Power Management
AT	Advanced Technology
ATA	AT Attachment
BAT	Basic Access Test
BBS	BIOS Boot Specification
BDA	BIOS Data Area
BGA	Ball Grid Array
BIOS	Basic Input and Output System
CMOS	Complementary Metal Oxide Semiconductor
CPLD	Complex Programmable Logic Device
CPU	Central Processing Unit
CRT	Cathode-ray Tube
DIM	Device Initialization Manager
DIMM	Dual Inline Memory Module
DMA	Direct Memory Access
DMI	Desktop Management Interface
DVMT	Dynamic Video Memory Technology
DVI	Digital Video Interface
EBDA	Extended BIOS Data Area
ECP	Extended Capabilities Port
EEPROM	Electrical Erasable Read Only Memory
E-IDE	Enhanced Integrated Drive Electronics
EMS	Expanded Memory System
ESCD	Extended System Configuration Data
EPP	Enhanced Parallel Port
FSB	Front Side Bus
GPNV	General Purpose Non-Volatile (RAM)
GTL	Gunning Transceiver Logic
HW	HardWare
IDE	Integrated Drive Electronics
IGD	Internal Graphic Device

G41-CPU-Desktop

IPL	Initial Program Load (Device)
LAN	Local Area Network
LBA	Logical Block Addressing
LCD	Liquid Crystal Display
MAC	Media Access Control
MTRR	Memory Type Range Register
MP	Multiple Processors
NA	Power failure
NVRAM	Non-volatile Random Access Memory
P-ATA	Parallel AT Attachment (old version of hard disk interface)
POS	Point of Sales
PCI	Peripheral Component Interconnect
PnP	Plug and Play
POST	Power On Self Test
RAM	Random Accessible Memory
RI	Ring Indicator
ROM	Read Only Memory
RS	Retail Systems
SATA	Serial AT Attachment (new version of hard disk interface)
SLP	System Locked Pre-Installation
SMI	System Management Interrupt
SMM	System Management Mode
SMRAM	System Management RAM
SPGA	Staggered Pin Grid Array
TFT	Thin-film transistor
TPM	Trusted Platform Module
UPS	Uninterruptible Power Supply
USB	Universal Serial Bus
UUID	Universal Unique IDentifier
VGA	Video Graphics Array
WOL	Wake On LAN
WOM	Wake On Modem

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